

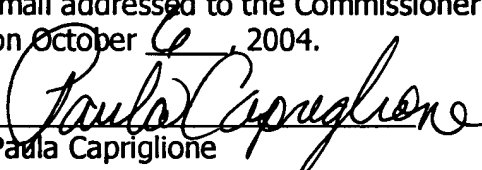
IFW

**PATENT**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In the Matter of the Application of: W. Palmteer et al.  
For: Surface Mount Package for a High Power Light Emitting Diode  
Serial No.: 10/696,005 Filed: October 29, 2003  
Examiner: N. Parekh Group Art Unit: 2811  
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail addressed to the Commissioner for Patents, Alexandria, VA 22313-1450 on October 6, 2004.

  
Paula Capriglione

**REPLY TO RESTRICTION REQUIREMENT**

Sir:

This reply is in response to the first Office Action mailed September 9, 2004 for the above identified application. This reply is being filed within the one month shortened statutory period for response set in the Office Action.

Charge any fee associated with this reply and credit any overcharge to Deposit Account No. 23-1950.

The Office has required Applicants to restrict the application to one of two inventions, identified as follow:

Group I, claims 1 – 8 and 14 – 17, drawn to a semiconductor device/system classified in class 257, subclass 433; and

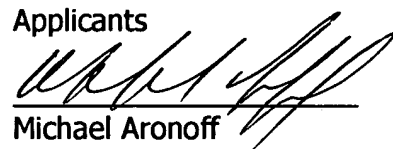
Group II, claims 9 – 13, drawn to a method of making a semiconductor device, classified in class 438, subclass 48.

Applicant elects to prosecute the invention of Group I, claims 1 – 8 and 14 – 17 in the present application. The election is made without traverse. However, Applicants expressly reserve the right to have claims 9 – 13 reinstated when allowable subject matter is indicated in claims 1 – 8 and/or 14 – 17.

Respectfully submitted,

W. Palmteer et al.  
Applicants

By:

  
Michael Aronoff  
Registration No. 37,770  
Attorney for Applicants  
Phone: (302) 633-2770  
Facsimile: (302) 633-2776